

FIG. 1C
(prior art)

FIG. 1D is a schematic diagram of a prior art circuit.

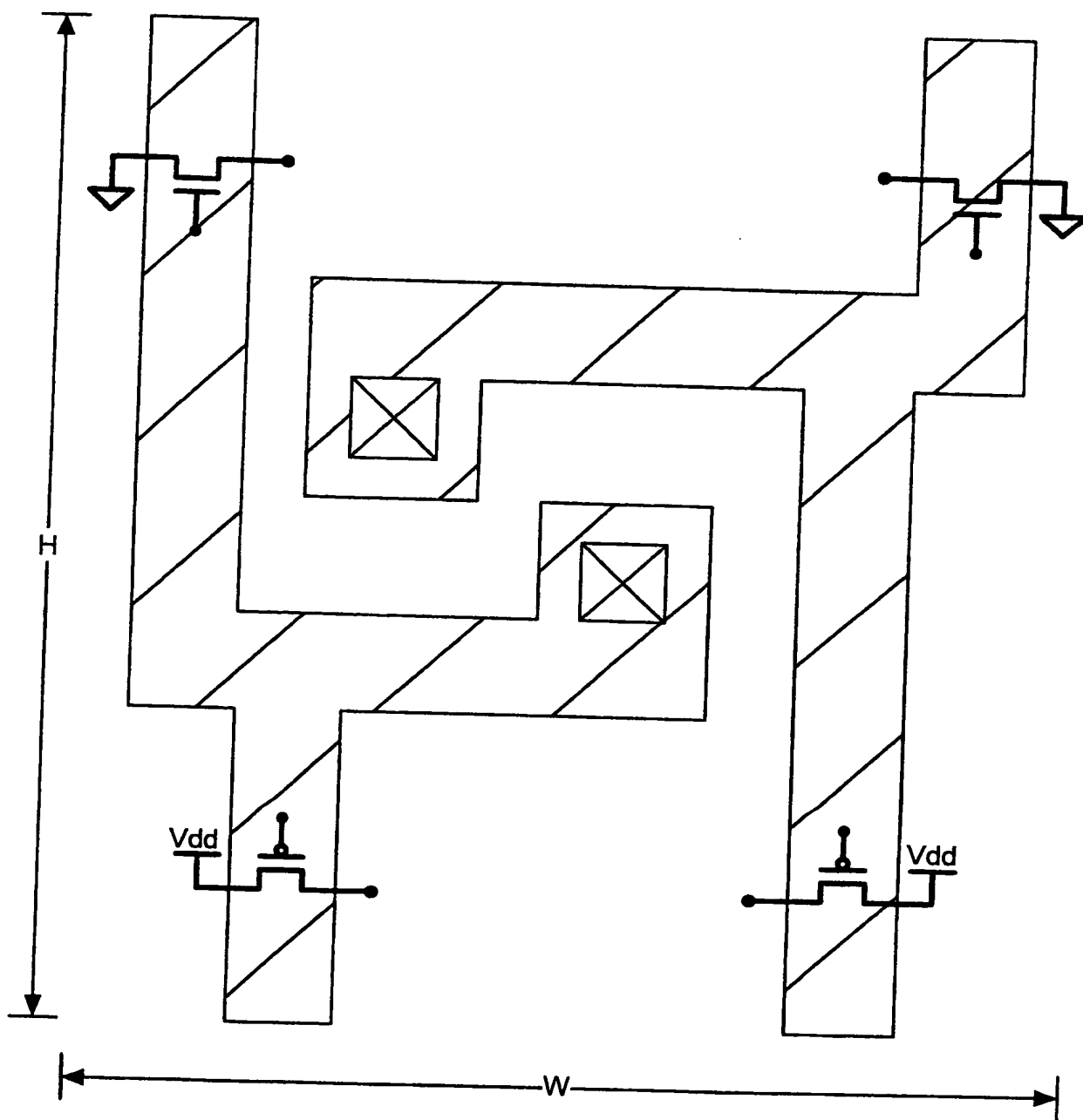


FIG. 1D
(prior art)

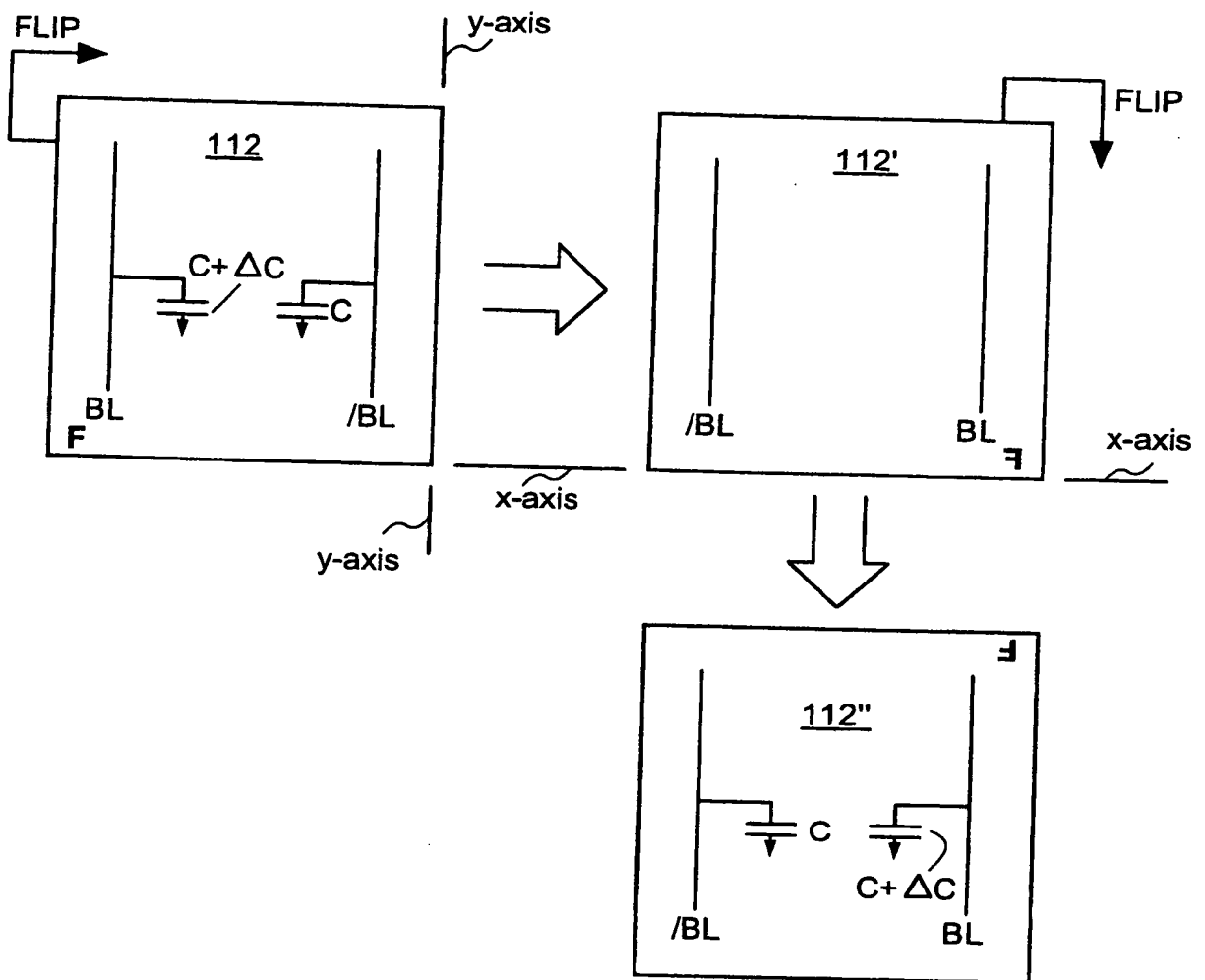
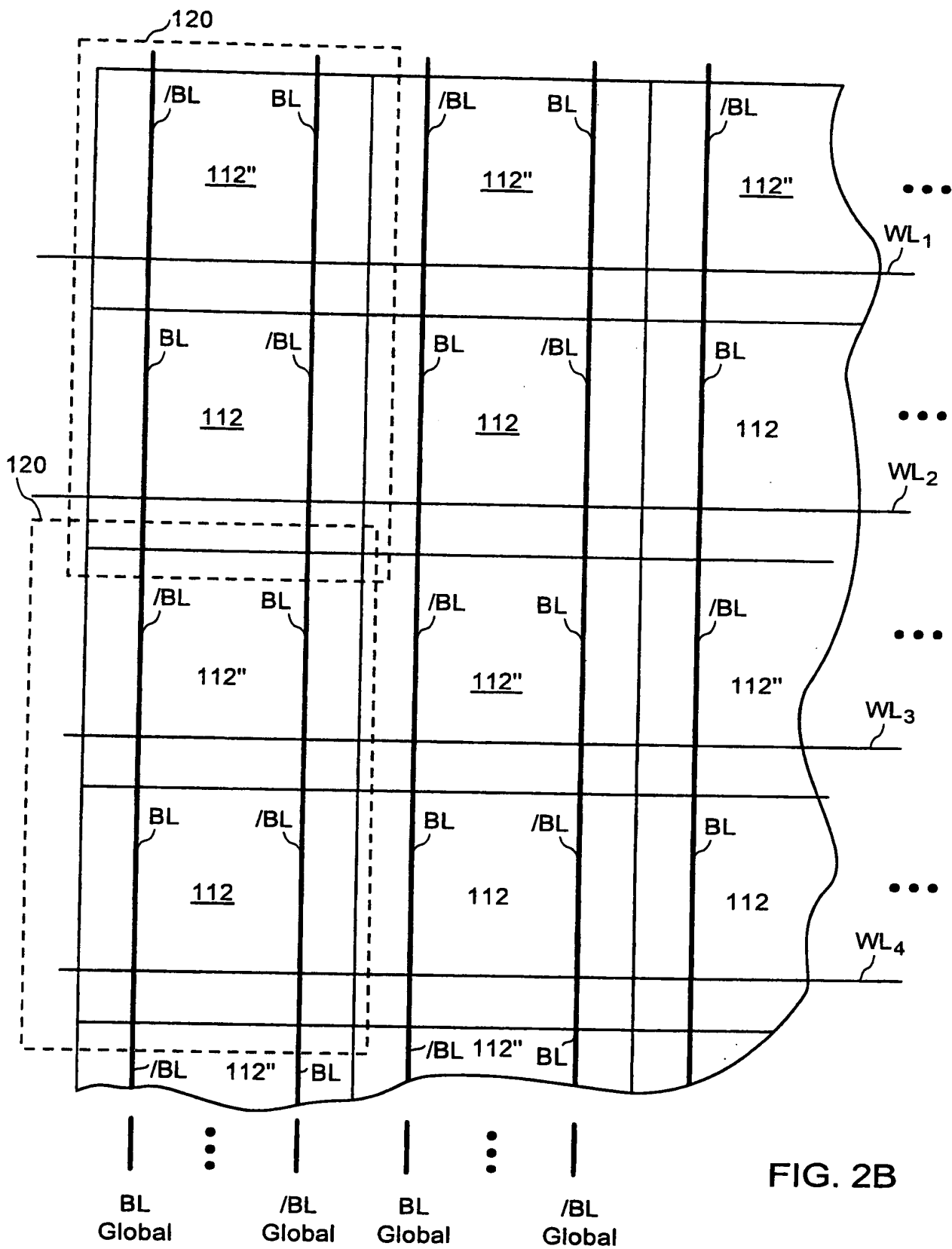


FIG. 2A



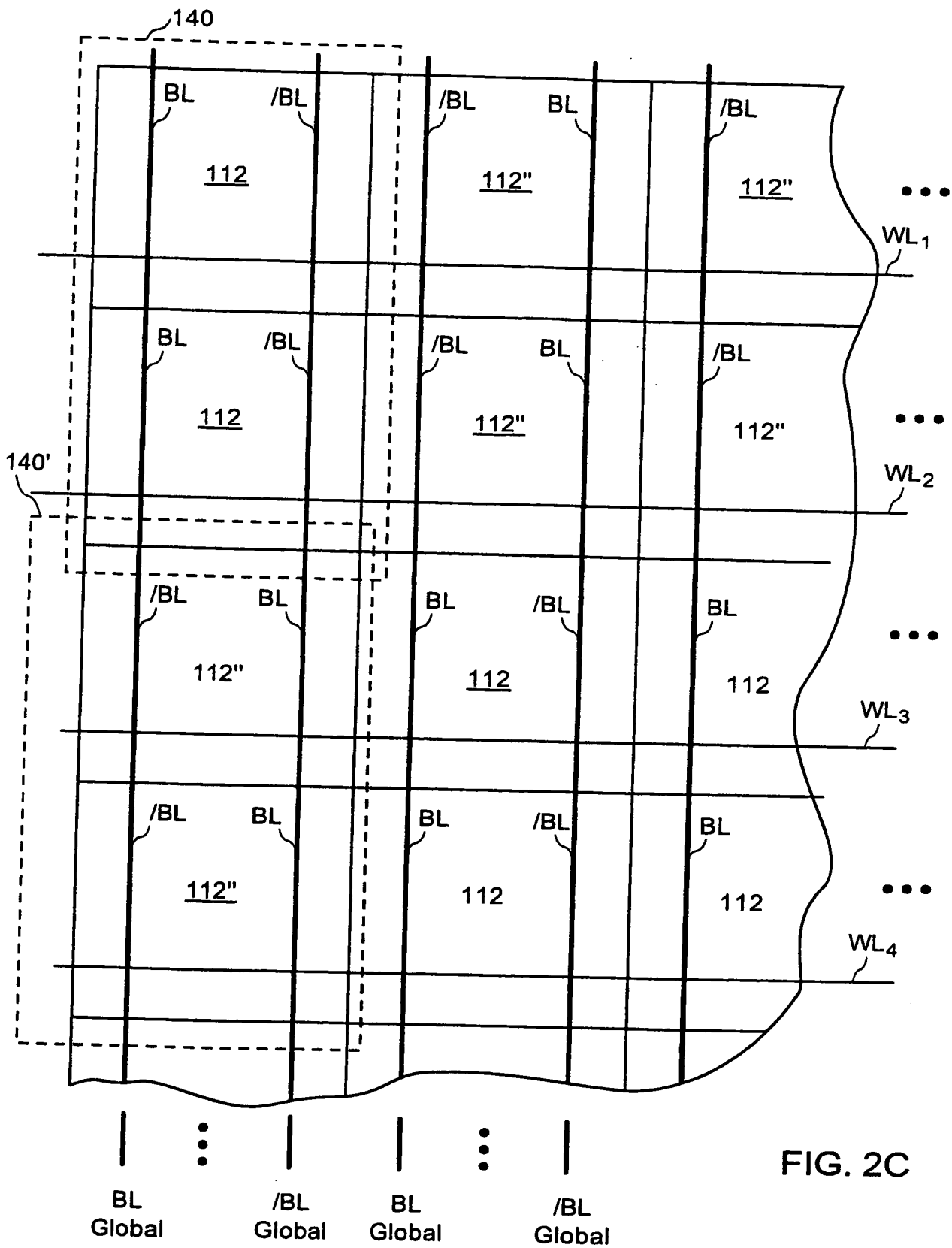


FIG. 2C

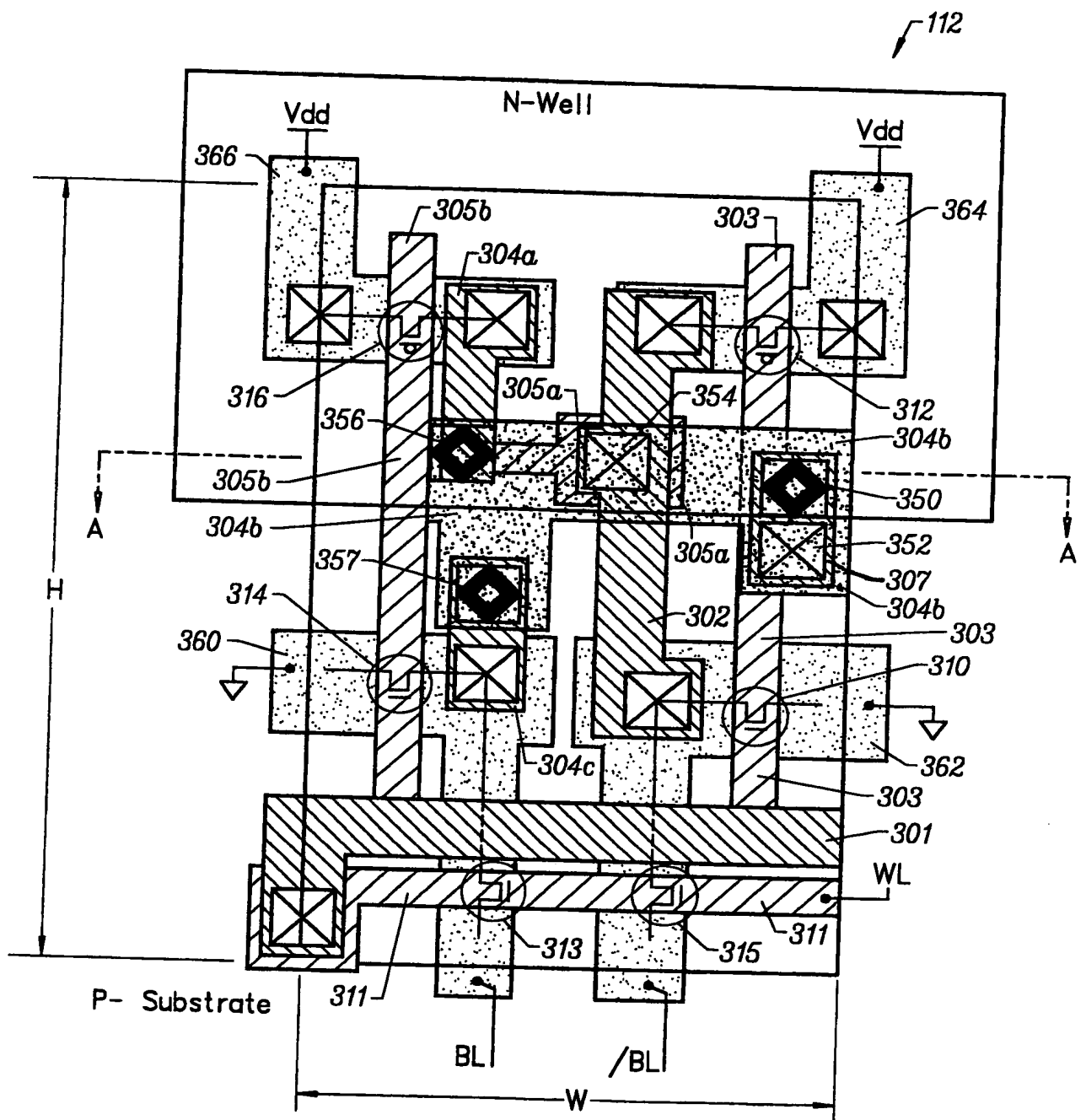


FIG. 3B

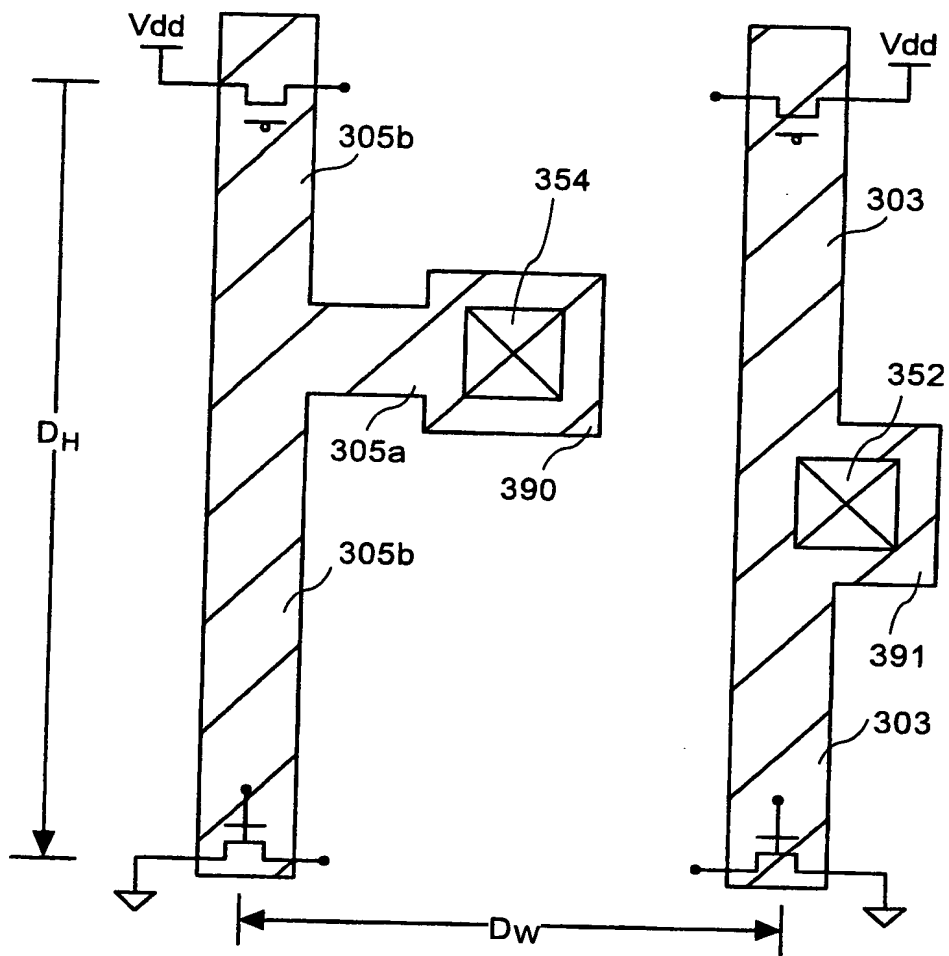


FIG. 3C

